

| ISSUE CLASSIFICATION | |
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| Class | Subclass |
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FILED UNDER 35 U.S.C. 371

PATENT NUMBER

U.S. UTILITY Patent Application

OLPE

PATENT DATE

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| APPLICANTS | APPLICATION NO. | CONT/PRIOR | CLASS | SUBCLASS | ART UNIT | EXAMINER |
| | 09/831763 | D F | 205 | 123 | 1741 | Smith/Hicks |
| TITLE | <p>Method and apparatus for the detection of integrated circuits of high-purity</p> | | | | | |

[illegible]

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| <input type="checkbox"/> TERMINAL DISCLAIMER | DRAWINGS | | CLAIMS ALLOWED | |
| | Sketch Drawg. | Figs. Drawg. | Print Fig. | Total Claims Print Claim for O.G. |
| <input type="checkbox"/> The term of this patent subsequent to _____ (Date) has been disclaimed. | _____ (Assistant Examiner) _____ (Date) | | NOTICE OF ALLOWANCE MAILED | |
| <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ | _____ (Primary Examiner) _____ (Date) | | ISSUE FEE | |
| <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed. | _____ (Legal Instruments Examiner) _____ (Date) | | Amount Due | Date Paid |
| | | | ISSUE BATCH NUMBER | |
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(Rev. 6/73)

Form PTO-433A
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